



Failure modes of vacuum plasma spray tungsten coating created on carbon fibre composites under thermal loads

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ABSTRACT

Vacuum plasma spray tungsten (VPS-W) coating created on a carbon fibre reinforced composite (CFC) was tested under two thermal load schemes in the electron beam facility to examine the operation limits and failure modes. In cyclic ELM-like short transient thermal loads, the VPS-W coating was destroyed sub-layer by sub-layer at 0.33 GW/m^2 for 1 ms pulse duration. At longer single pulses, simulating steady-state thermal loads, the coating was destroyed at surface temperatures above $2700 \text{ }^\circ\text{C}$ by melting of the rhenium containing multilayer at the interface between VPS-W and CFC. The operation limits and failure modes of the VPS-W coating in the thermal load schemes are discussed in detail.

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1. Introduction

The primary ITER materials choice is a full beryllium (Be) main chamber wall with carbon fibre reinforced composite (CFC) at the strike points and tungsten (W) at the dome and the upper part of the divertor vertical targets. Neither the reference material combination (Be Wall/CFC + W divertor) nor its possible combination in the future (Be Wall/W divertor) have ever been tested in a tokamak. Therefore, the ITER-like Wall (ILW) Project has been launched at the JET tokamak [1,2]. The lessons we can learn from such an experiment in JET would be invaluable for reducing the risk of plasma-facing material related issues in ITER.

In the ILW Project, the JET tokamak will employ W-coated CFC tiles for the outer and inner divertor rows. For this application, a

200 μm thick vacuum plasma spray (VPS) W coating was selected at the end of the R&D program in 2006 [3–6]. During normal operation the W coating at the JET divertor will be exposed to intense transient heat loads as well as to steady-state thermal loads. Therefore, it is indispensable to assess the operation limits and failure modes of the coating under such thermal loading conditions.

In the present work, the operation limits and failure modes of the coating were studied under high thermal loads: (1) ELM-like short transient thermal loads; and (2) longer pulse loads simulating steady-state thermal loads in the electron beam facility.

2. Tungsten coating for thermal loading tests

The 200 μm W coating was produced by vacuum plasma spray technology [7] by Plansee AG. Thick coating was regarded to be advantageous since the major fraction of the heat dissipates in the thick coating before arriving at the interface during a short pulse. The characteristic heat propagation distance is considered

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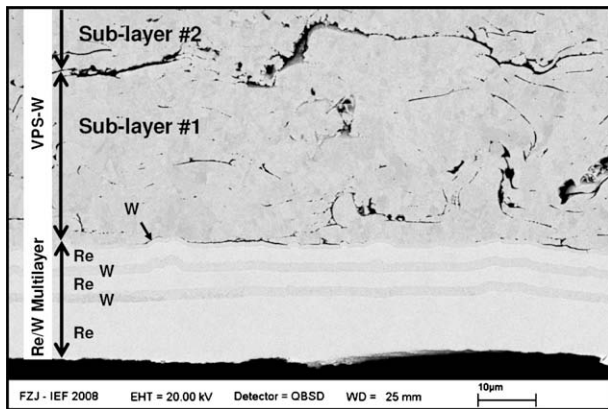


Fig. 1. Cross section of the VPS-W coating and the multilayer interlayer.

to be in a range of 100 μm during 1 ms assuming a lower thermal conductivity of the VPS-W coating than a solid W (e.g., the heat propagation distance in a solid W is estimated to be several hundreds μm in 0.5 ms [8]). Thus, the coating interface is not exposed to the thermal fatigue caused by the multiple short transient thermal loads. In fact, thinner W coatings (4 and 10 μm) were sensitive to such thermal loads since each heat pulse affected the interfaces [3,4].

The cross section around the coating interface is shown in Fig. 1. The W coating has a sophisticated interlayer consisting of a Rhenium (Re) and W multilayer. The Re/W multi-layer interlayer was aimed to be a diffusion barrier against carbon, preventing the formation of tungsten carbide in the main W coating [9,10]. The soft Re layers would also provide a compliant interface between the CFC and the hard W layers in the coating system. The multilayer interlayer was created by Physical Vapour Deposition (PVD) by alternating the deposition sources and processes, i.e., Re layers by arc deposition and W layers by sputtering deposition [11]. The multi-layer consisted of optimized six layers; 10 μm -Re, 2 μm -W, 3 μm -Re, 2 μm -W, 2.5 μm -Re and 1 μm -W, starting from the CFC surface to the VPS-W coating layer. The VPS-W coating was created in a vacuum at a high temperature; during the VPS process, the plasma torch delivers a power density in the order of 5 MW/m² onto the coating surface. The high process temperature can cause complicated residual stresses in the coating due to anisotropic thermal expansion mismatches between the W coating and CFC substrate. In addition, these residual stresses could cause tensile cracks in the W coating even just after production [6].

The W coating sample was produced on a two-directional CFC, (2D-CFC, DMS780 Dunlop). The CFC has a fibre-bundle architecture based on cross-ply laminates (x - and y -directions). PAN (polyacrylonitrile) fibre-bundles are utilized for the laminates. These fibre-bundles guarantee the high mechanical strength and thermal conductivity of the CFC in the two directions along the fibre-bundles. The carbon fibre-bundles have anisotropic thermal properties that originate from the nature of pyrolytic graphite [12] and the constituents [13]. Indeed, along the fibre direction, the thermal expansion is low (around $0\text{--}1 \times 10^{-6} \text{K}^{-1}$) and the thermal conductivity high, while in the fibre radial direction the thermal expansion is higher ($10\text{--}13 \times 10^{-6} \text{K}^{-1}$) and the thermal conductivity lower. The CFC contains felt layers sandwiched between the two cross-ply laminates as a compliant layer. Both fibre-bundle layers and felt layers have typically about 0.5 mm thickness. The W coating was produced on a CFC surface containing perpendicular and parallel PAN fibre-bundles (Fig. 5(b)).

For the thermal tests, a large flat area of a CFC block which was roughly the half size of a JET divertor tile (G7 tile), $100 \times 300 \text{mm}^2$, was coated. It is worth mentioning that during VPS coating, the

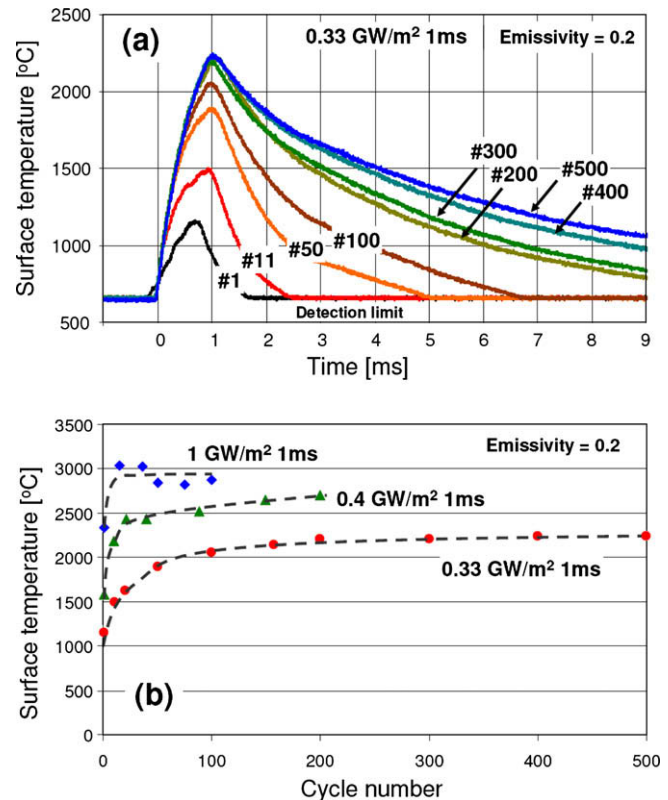


Fig. 2. (a) Time evolution of surface temperatures under thermal loads of 0.33 GW/m² for 1 ms over 500 pulses. Note that the data acquisition was triggered by a defined surface temperature level. Due to the slow temperature increase, the data acquisition of the first pulse started later. Therefore, the pulse duration of the first shot appeared to be short, although the pulse duration was the same as for the following shots. (b) Maximum surface temperature as a function of cycle number at the surfaces loaded at 0.33, 0.4, and 1 GW/m² for 1 ms.

plasma torch moved across the surface to be coated from the starting point to end point and returned to the starting point [9]. During such a back and forth process, the atmosphere and substrate temperature have to be well controlled, especially for large surfaces; otherwise, sub-layers can be created by each pass of the plasma torch due to non-uniform stress and deposition of material from the halo of the plasma torch.

3. High heat flux test in the electron beam facility JUDITH

High heat flux (HHF) tests were performed using the electron beam facility, JUDITH, at the Forschungszentrum Jülich, Germany [14,15]. The maximum beam power is 60 kW. The beam was focused to a diameter of approximately 1 mm. Fairly homogenous heat distribution is realized by fast scanning of the electron beam (typically 40 kHz in x -direction and 31 kHz in y -direction) at a well-defined area. The maximum beam scanning area is $100 \times 100 \text{mm}^2$ at the test position. Absorbed power density was calculated as the product of the acceleration voltage and the absorbed beam current. The acceleration voltage was held constant and the beam current was varied to provide defined heat loads. Although the acceleration voltage is high (120 kV), the penetration depth of the electron beam in solid tungsten was calculated to be up to 5 μm . Considering the thickness of the W coating (200 μm), the loading can be regarded as a surface heat flux.

ELM-like short transient thermal loading was applied by a 1 ms pulse with a rectangular waveform over a well-defined area of 16 mm² at room temperature. This load was repeated for 1000 cycles. The increase of bulk temperature of the W-coated CFC block

was negligible due to the large heat capacity of the CFC block. In these HHF tests, the power density limit and failure mode were examined.

Longer pulses were applied over an area of 64 mm^2 in order to simulate the steady-state heat load. It was necessary to limit pulse duration and the loading area to realize loads at high energy density and minimize the energy deposition into the sample. The pulse duration was selected to be 2 s which is much longer than the material time constant (characteristic time for heat propagation through the $200 \text{ }\mu\text{m}$ thick W coating and interlayer to the CFC substrate, which is estimated to be in a range of several ms). The loaded area was designed to cover several pairs of cross-ply laminates of the underlying CFC laminate structure. Single pulse loads at virgin coating surfaces were applied at around room temperature. The bulk temperature increased up to $50 \text{ }^\circ\text{C}$ after the pulses during the tests. In these HHF tests, the operational temperature limit and failure mode were examined.

The bulk temperatures were monitored with an infra-red camera, AGEMA THV900 (detection wavelength: around $3.5 \text{ }\mu\text{m}$) through a CaF_2 window. In addition, the time evolution of the averaged surface temperature in a 3–4 mm diameter spot was observed with a single colour pyrometer (detection wavelength: around $1.5\text{--}1.6 \text{ }\mu\text{m}$; detection limit $633 \text{ }^\circ\text{C}$) through a quartz window. In both cases, the emissivity was assumed to be 0.2 over the measured temperature range.

4. Failure mode and power density limits of VPS-W coating under ELM-like short transient thermal loads

Fig. 2(a) shows the time evolution of surface temperature under heat loads of 0.33 GW/m^2 for 1 ms, over 500 cycles. As it is illustrated in this figure, the maximum surface temperature for the first shot was around $1200 \text{ }^\circ\text{C}$. The maximum surface temperature increases with the number of shots and reaches $2200 \text{ }^\circ\text{C}$ after 200 shots. The maximum surface temperature increased drastically at the beginning of the cyclic loading and it saturated at a later stage (Fig. 2(b)). At higher power densities the temperature increase saturated at an earlier stage. This indicates the early completion of the coating destruction due to higher thermal stresses induced by higher power density loads. Another remarkable finding was a significant change of the cooling rates. After the first cycle, the surface temperature decreased rapidly, whereas after the 50th cycle, one can observe a noticeable slow down of the cooling rate. This indicates a poor thermal contact of the coating layers to the underlying substrate leading to a less efficient heat removal.

Fig. 3(a) shows the microstructure of the surface loaded at 1 GW/m^2 for 1 ms for 100 cycles. The coating showed surface damage that consisted of small droplets and peeling of sub-layers of the VPS-W coating. Numbers of cracks were generated at the loaded surfaces during the cyclic loading [16]. The sub-layers started to melt at the crack edges which had small local heat capacity. The micrograph (Fig. 3(a)) indicates the destruction processes: (i) cracking of a sub-layer, (ii) melting of crack edges of the sub-layer (marked as A), (iii) removal/extraction of the sub-layer due to melting/re-solidification of the molten sub-layer, (iv) cracking of the lower sub-layer due to thermal exposure (marked as B), and (v) melting of crack edges of the sub-layer (marked as C).

Fig. 3(b) and (c) show the cross section of the coating loaded at 0.4 GW/m^2 for 1 ms after 200 pulses. The coating was removed sub-layer by sub-layer due to the poor mechanical and thermal contact of each sub-layer. The VPS-W coating consisted of seven sub-layers as shown in Fig. 3(c). After removal/delamination of the sub-layers, melting of the sub-layer occurred because of the small heat capacity of the detached layers. Two sub-layers were removed after 200 cycles at 0.4 GW/m^2 for 1 ms loading as shown in Fig. 3(c). The same destruction process was observed after 500

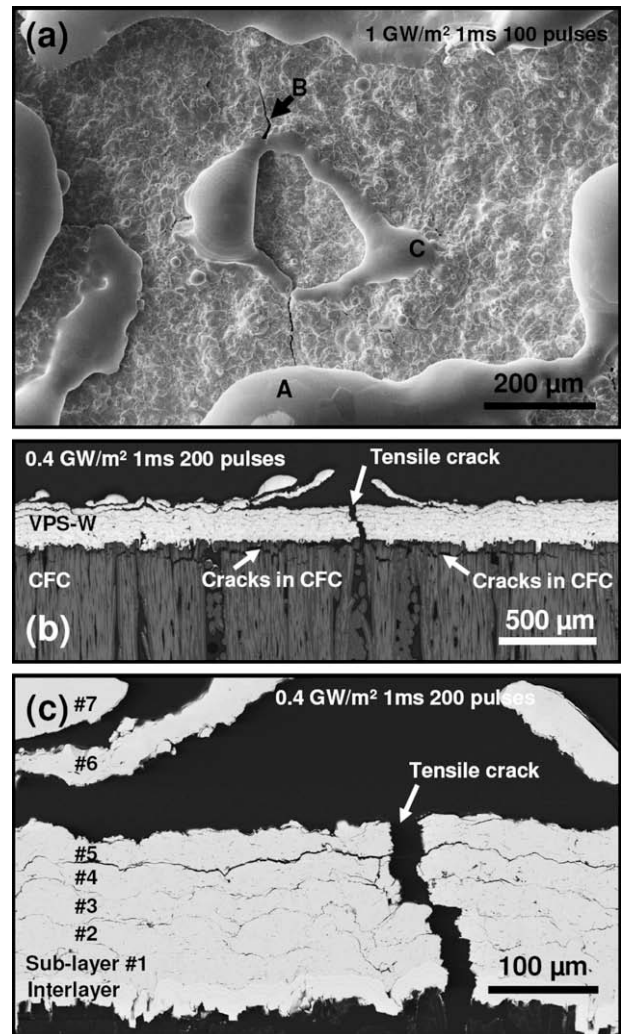


Fig. 3. (a) Microstructure of the loaded surface at 1 GW/m^2 for 1 ms over 100 cycles, (b) and (c) cross section of the loaded area at 0.4 GW/m^2 for 1 ms over 200 cycles.

cycles at 0.33 GW/m^2 for 1 ms. Fig. 3(b) and (c) also shows a tensile crack [6] which existed before the experiments, propagating through the entire VPS-W coating thickness and running across the loaded and non-loaded coating surfaces in perpendicular to the cross-ply fibre-bundle laminates of CFC. The tensile crack width in the image was around $20 \text{ }\mu\text{m}$.

The sub-layer structure in the VPS-W coating was not observed in the previous tests of 'small sample' ($\sim 80 \text{ mm} \times \sim 80 \text{ mm}$) which was prepared by the same technology as the large samples studied here. Indeed, the previous tests on the 'small sample' did not show any damage at 0.33 GW/m^2 for 1 ms over 1000 cycles [3]. The high resistance was likely associated with a monolithic structure of the thick coating without having sub-layers in the 'small sample'. It is concluded that the formation of the sub-layers during the production process was the critical point, which is assumed to be the result of scaling the process to large areas where deposition from the halo of the plasma torch and surface temperature are not as easy to control. In such VPS process, each sub-layer corresponds to a single pass of the plasma torch across the tile surface.

The multilayer did not show microstructural modification by this short pulse loading (Fig. 3(c)) since the coating was thermally thick, and therefore, the pulse duration was shorter than characteristic time which is estimated to be in a range of several ms for heat propagation through the coating to the interlayer. Thus, the coating

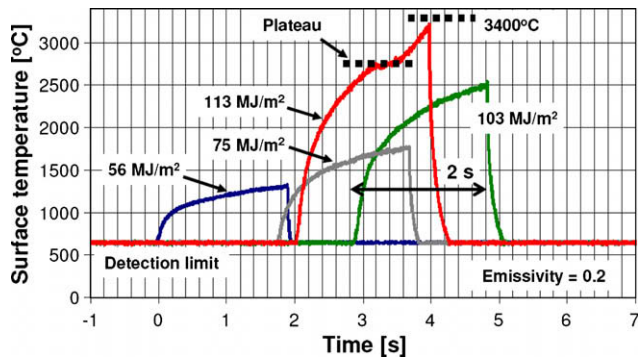


Fig. 4. Time evolution of the surface temperature during steady-state loads for 2 s.

interface was not strongly exposed to the thermal fatigue caused by the multiple short transient thermal loads.

Cracks developed horizontally in the CFC part were formed beneath the coating layer (Fig. 3(b)) due to poor mechanical stability. The cracks extended to a range of mm around the tensile cracks. These horizontal cracks caused an increase of the surface temperature and a low cooling rate because of the poor thermal contact.

5. Failure mode and temperature limits of VPS-W coating under steady-state thermal loads

Fig. 4 shows the time evolution of the surface temperature at the VPS-W coating during thermal loads at various energy densities. The coating was not damaged below 103 MJ/m^2 for 2 s [16]. The destruction occurred above 113 MJ/m^2 for 2 s. The measured averaged surface temperature at this energy density reached up to 3200°C . As shown in the Fig. 4, the surface temperature showed a plateau at around 2700°C (the temperature could be locally higher within the measured surface area). This plateau indicates melting of the coating. The temperature at the plateau was far below the melting temperature of W (3400°C). This is due to melting of the interlayer after W–Re alloy and/or tungsten carbide formation, which would be created by mutual diffusion in the multilayer at high temperature. The melting points of the W–Re alloy and tungsten carbide go down to 2825 and 2715°C , respectively [17]. This is the thermally weakest point in the coating system.

Fig. 5(a) shows a microstructure of the surface loaded at 113 MJ/m^2 for 2 s. At this energy density the coating showed melting. Two holes with a distance of about 2 mm were found at the loaded surface. CFC surfaces were found through the holes, which were caused by the poor wetting of the molten W alloy at the carbon surface. This separation between the two holes is due to the structure of the underlying CFC substrate as shown in Fig. 5(b). This microstructure implies that melting started at the multilayer interlayer around the CFC surface as discussed above. In the re-solidified layer at the top surface, W as well as small amounts of Re and C were detected by energy dispersive spectrometry (EDS) analysis [16].

The coating did not show any remarkable damage, e.g., melting, cracking, below 113 MJ/m^2 for 2 s [16]. It indicates that the coating had a clear damage threshold which is defined by the surface temperature. One must note that the threshold energy density is not relevant in this experiment since the energy dissipates in a large mass of the sample after being deposited at a small loaded area. Thermal loading at the entire sample surface was required to define the threshold energy density [18], which was not considered in these experiments.

Fig. 5(b) and (c) show cross sections of the damaged VPS-W coating. The section line was near the crater. The sectioning was

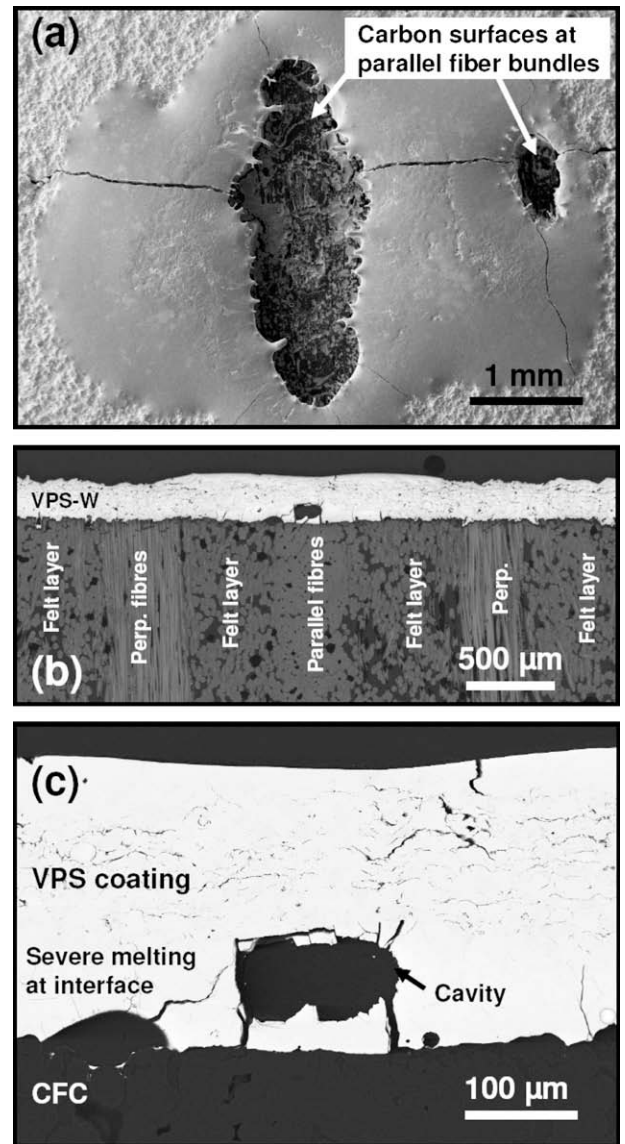


Fig. 5. Microstructure of the loaded area at 113 MJ/m^2 for 2 s, single pulse: (a) microstructure of the loaded surface; (b) and (c) cross section of the loaded area.

performed to include parallel and perpendicular fibre bundles in the cross section, namely, in parallel to the tensile crack shown in Fig. 5(a) and therefore the cracks did not appear in Figs. 5(b) and (c). As mentioned above, the melting of the coating depends on the underlying CFC structure. Fig. 5(b) shows that the coating melted at the fibre-bundle parallel to the surface where the heat transfer into the bulk was low. Here was the starting point of melting due to overheating. Severe melting was observed at the interlayer as shown in Fig. 5(c). Above the molten layer at around the interlayer, VPS-W sub-layers could still be observed and appear unaffected. This microstructure confirms that the destruction of the coating started by melting at the interlayer but not at the top surface. At the top surface, a re-solidified layer was observed, which originated from the surrounding crater near the section line. Consequently, the destruction processes of the coating under steady-state thermal loads are: (i) overheating at the parallel fibre-bundles of the CFC substrate; (ii) decrease of melting temperature by formation of W–Re alloy and/or tungsten carbide in the multilayer interlayer; (iii) melting of the interlayer; (iv) mass transport of molten phase during heating; and (v) a formation of cavities

during the cooling phase. The poor wetting of the molten W alloy at the carbon surface caused the large crater and open carbon surfaces at the damaged area.

6. Summary and concluding remarks

Thermal load tests were performed on a 200 μm thick VPS-W coating created on a 2D CFC with a W/Re multilayer interlayer. The thermal load tests were multiple short pulse loadings, i.e., ELM-like short transient thermal load tests, and longer single-pulse loading, i.e. steady-state thermal load tests.

Under ELM-like short transient thermal loads, the coating showed surface damage such as small droplets and peeling of sub-layers in the VPS-W layers. The damage threshold in power density was below 0.33 GW/m^2 1 ms. The formation of the sub-layers was the critical feature with respect to the performance of the VPS-W coating under the short thermal loads. Under steady-state thermal loads, the coating showed melting of the multilayer interlayer at the locally overheated parallel fibre-bundle surfaces. The formation of W–Re alloy and/or tungsten carbide caused decreases in the melting temperature of the coating. The damage threshold in operation temperature was found to be the melting points of the W–Re alloy and/or tungsten carbide, 2825 and 2715 $^{\circ}\text{C}$, respectively.

As a result of numbers of experimental observations of failure of the thick W coatings under heat load tests, the ILW Project has decided to exclude the 200 μm VPS-W coating from the Project baseline. Thinner PVD-W coatings are being developed and are being qualified for the ILW Project [19].

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